Refine Search

Search Results -

Terms	Documents
(("low pin count" or LPC) near5 memory) same ((firmware or BIOS) near5 memory)	28

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:
L1

Refine Search

Recall Text Clear Interrupt

Search History

DATE: **Thursday, December 29, 2005**   Printable Copy   Create Case

Set Name Query	Hit Count	Set Name
side by side		result set

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

Set Name	Query	Hit Count	Set Name
L1	(("low pin count" or LPC) near5 memory) same ((firmware or BIOS) near5 memory)	28	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 and L3	15

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L4

Refine Search

Recall Text   Clear   Interrupt

Search History

DATE: Thursday, December 29, 2005   Printable Copy   Create Case

**Set
Name Query**
side by
side

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L4   l1 and L3

L3   710/305,33,74,1,315,316;709/228,230,250;711/149,100,145,211;713/1,100;704/7;365/63,189.08,230.05.ccls. 1;

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L2   L1

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L1   (("low pin count" or LPC) near5 memory) same ((firmware or BIOS) near5 memory)

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1	0

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L2

[Refine Search]

[Recall Text ⬍] [Clear] [Interrupt]

Search History

DATE: **Thursday, December 29, 2005**   Printable Copy   Create Case

Set Name	Query	Hit Count	Set Name
side by side			result set
	DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
L2	L1	0	L2
	DB=PGPB,USPT,USOC; PLUR=YES; OP=OR		
L1	(("low pin count" or LPC) near5 memory) same ((firmware or BIOS) near5 memory)	28	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
"1-bit" near5 "memory flag"	3

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L2

[Refine Search]

[Recall Text] [Clear] [Interrupt]

Search History

DATE: Thursday, December 29, 2005   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=PGPB,USPT,USOC; PLUR=YES; OP=OR		
L2	"1-bit" near5 "memory flag"	3	L2
L1	"1-bit memory flag"	0	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
"1-bit" near5 "memory flag"	1

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:
L3

Refine Search

Recall Text   Clear   Interrupt

Search History

DATE: Thursday, December 29, 2005   Printable Copy   Create Case

Set Name	Query	Hit Count	Set Name
side by side			result set
DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L3	"1-bit" near5 "memory flag"	1	L3
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
L2	"1-bit" near5 "memory flag"	3	L2
L1	"1-bit memory flag"	0	L1

END OF SEARCH HISTORY

EAST - [Untitled1:1]

File View Edit Tools Window Help

Drafts
Pending
Active
  L1: (161) (("low pin co
  L2: (10) l1 same ((firm
Failed
Saved
Favorites
Tagged (0)
UDC
Queue
Trash

DBs USPAT

Default operator OR

☑ Plurals
☑ Highlight all hit terms initially

BRS form   IS&R form   Image   Text   HTML

	Type	L #	Hits	Search Text	DBs	Time Stam	Comment	Error Definit	Er
1	BRS	L1	161	(("low pin count" or LPC) near5 memory)	USPA T	2005/12/2 9 19:19			
2	BRS	L2	10	l1 same ((firmware or BIOS) near5 memory)	USPA T	2005/12/2 9 19:19			

Start   EAST - [...

EAST - [Untitled1:1]

File   View   Edit   Tools   Window   Help

- Drafts
- Pending
- Active
  - L1: (161) (("low pin co
  - L2: (10) l1 same ((firm
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

Search | List | Browse | Queue | Clear

DBs | USPAT

Default operator | OR ▼

☑ Plurals
☑ Highlight all hit terms initially

```
l1 same ((firmware or BIOS) near5 memory)
```

BRS form | S&R form | Image | Text | HTML

	U	I	Document ID	Issue Dat	Pages	Title	Current OR	Current XR
1	☐	☐	US 6952751 B1	20051004	14	Method and apparatus for extending legacy co	710/315	710/106
2	☐	☐	US 6944064 B2	20050913	8	Memory unit having programmable device ID	365/189.01	365/185.33 ;
3	☐	☐	US 6927991 B2	20050809	9	Memory device accessible with differe	365/63	365/189.07 ;
4	☐	☐	US 6904484 B1	20050607	7	Low pin count (LPC) firmware hub recovery	710/300	365/185.08 ;
5	☐	☐	US 6851014 B2	20050201	13	Memory device having automatic protocol dete	711/103	711/145; 713/2
6	☐	☐	US 6789169 B2	20040907	11	Embedded DRAM cache memory and method havin	711/122	711/140; 711/144;
7	☐	☐	US 6505263 B1	20030107	21	Bus controller operating code in syste	710/100	710/107; 710/305;
8	☐	☐	US 6477603 B1	20021105	6	Multiple PCI adapters within single PCI slot	710/301	361/785; 439/74
9	☐	☐	US 6421765 B1	20020716	9	Method and apparatus for selecting functiona	711/145	711/100; 711/154;
10	☐	☐	US 6381693 B2	20020430	10	Arrangements having firmware support for di	713/1	713/2

Start | EAST - [...

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Search Results

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Results for "((low pin count) or lpc<in>metadata) and (firmware or bios)"
Your search matched **7** of **1286976** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

✉ e-mail 🖨 printer friendly

⊕ Search Options

View Session History

New Search

☐ Check to search only within this results set

Modify Search

[((low pin count) or lpc<in>metadata) and (firmware or bios)] [»]

Display Format: ⦿ Citation   ○ Citation & Abstract

⊕ Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Select   Article Information

☐ 1. **Voice Coding and Tree Encoding Speech Compression Systems Based Upon Inverse Filter Matching**
Matsuyama, Y.; Gray, R.;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 30, Issue 4, Apr 1982 Page(s):711 - 720

AbstractPlus | Full Text: PDF(1056 KB)   IEEE JNL

☐ 2. **Implementation of a Two-Channel LPC Vocoder**
Byoung Shin; Hyeong Lee; Chong Un; Sang Lee; Kil Song;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 31, Issue 7, Jul 1983 Page(s):907 - 916

AbstractPlus | Full Text: PDF(824 KB)   IEEE JNL

☐ 3. **Implementation of a Multirate Speech Digitizer**
Byung Lee; Chong Un; Hyeong Lee; Byoung Shin; Hwang Lee;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 31, Issue 6, Jun 1983 Page(s):775 - 783

AbstractPlus | Full Text: PDF(928 KB)   IEEE JNL

☐ 4. **The Impact of Mpact 2**
Purcell, S.;
Signal Processing Magazine, IEEE
Volume 15, Issue 2, March 1998 Page(s):102 - 107
Digital Object Identifier 10.1109/79.664703

AbstractPlus | Full Text: PDF(612 KB)   IEEE JNL

☐ 5. **A 2000-MOPS embedded RISC processor with a Rambus DRAM controller**
Suzuki, K.; Daito, M.; Inoue, T.; Nadehara, K.; Nomura, M.; Mizuno, M.; Iima, T.; Sato, S.; Fukuda, T.; Arai, T.; Kuroda, I.; Yamashina, M.;
Solid-State Circuits, IEEE Journal of
Volume 34, Issue 7, July 1999 Page(s):1010 - 1021
Digital Object Identifier 10.1109/4.772417

AbstractPlus | References | Full Text: PDF(512 KB)   IEEE JNL

☐ 6. **IEEE standard for high-bandwidth memory interface based on Scalable Coherent Interface (SCI) signaling technology (RamLink)**
IEEE Std 1596.4-1996
15 Sept. 1996

AbstractPlus | Full Text: PDF(968 KB)   IEEE STD

☐ 7. **IEEE standard for scalable coherent interface (SCI).**
IEEE Std 1596-1992

2 August 1993 Page(s):i

AbstractPlus | Full Text: PDF(5684 KB)   IEEE STD